

# Technical proposal for JX-LCC4-617M4200M-S40 LC Combiner

- ◆ Small Volume
- ◆ High Reliability

## Revision History

| Version | Date       | Author    | Revision description | Comment |
|---------|------------|-----------|----------------------|---------|
| V1.0    | 2020/08/31 | Lin liqun | Created              | /       |
|         |            |           |                      |         |
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### 1. Mechanical layout

Table 1 Mechanical specifications @ JX-LCC4-617M4200M-S40

|            |                            |
|------------|----------------------------|
| Dimension  | LxWxH:75mm x 50mm x 15mm   |
| Connectors | SMA-Female                 |
| Finish     | Conductive oxidation       |
| Notes      | Material shall be RoHS 6/6 |

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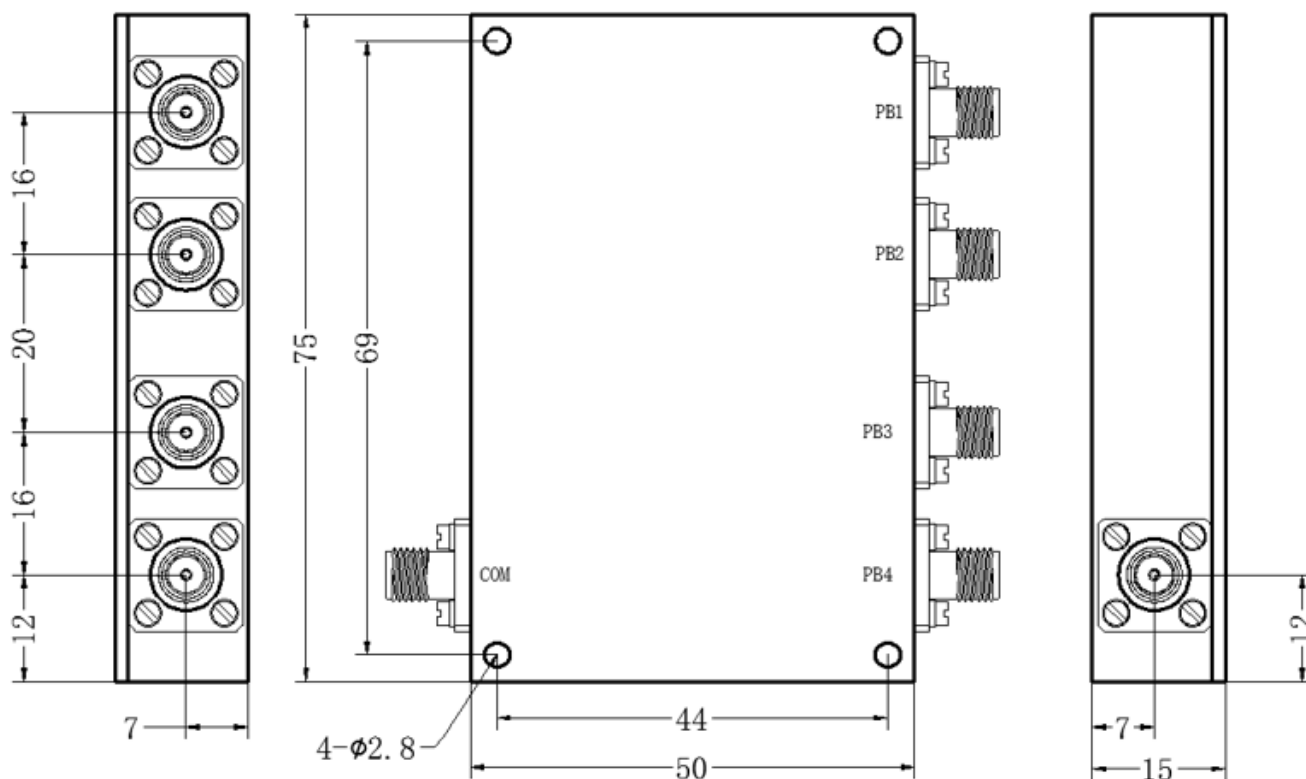


Figure 1 JX-LCC4-617M4200M-S40 front panel layout

## 2. Electrical analysis

### 2.1 Electrical specifications

Table 2 Electrical specifications @ JX-LCC4-617M4200M-S40

| Item | Parameter       | Specifications |              |              |              |
|------|-----------------|----------------|--------------|--------------|--------------|
|      |                 | PB1            | PB2          | PB3          | PB4          |
| 1    | Port            |                |              |              |              |
| 2    | Frequency range | 617-960MHz     | 1700-2200MHz | 2300-2700MHz | 3300-4200MHz |
| 3    | Insertion loss  | ≤2.0dB         | ≤3.5dB       | ≤3.5dB       | ≤2.0dB       |
| 4    | Return loss     | ≥12dB          |              |              |              |
| 5    | Isolation       | ≥40dB          |              |              |              |
| 6    | Power           | 2W CW          |              |              |              |
| 7    | Impedance       | 50 Ω           |              |              |              |

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## 2.2 The simulation curve

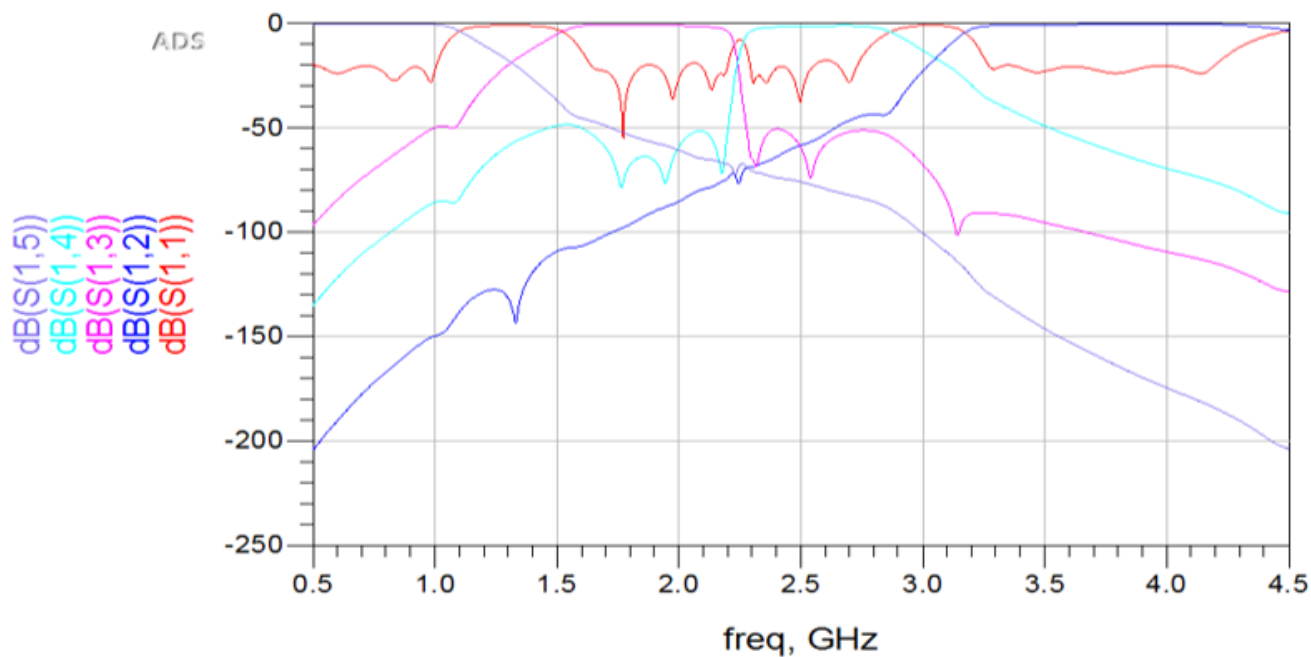


Figure 2 The simulation curve

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